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MAR 20 2003

Substitute for form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

1

Application Number

09/742,171

Filing Date

12/19/00

First Named Inventor

Steven Teig

Group Art Unit

2825

Examiner Name

A.M. Thompson

Attorney Docket Number

SPLX.P0017

## U.S. PATENTS RELATED TO THE PRESENT APPLICATIONS

Examiner* Initials	Cite No.	U.S. Patent Document Number Kind Code* (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Relation to Present Application
	1.	6,516,455 B1	Teig, et al.	02-04-03	

## U.S. PATENT DOCUMENTS

Examiner* Initials	Cite No.	U.S. Patent Document Number Kind Code* (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	2.	2001/ 0003843 A1	Scepanovic et al.	06-14-01	
	3.	5,097,422	Corbin, II et al.	03-17-92	
	4.	5,532,934	Rostoker	07-12-96	
	5.	5,566,078	Ding et al.	10-15-96	
	6.	5,578,840	Scepanovic et al.	11-26-96	
	7.	5,650,653	Rostoker et al.	07-22-97	
	8.	5,742,086	Rostoker et al.	04-21-98	
	9.	5,757,656	Hershberger et al.	05-26-98	
	10.	5,777,360	Rostoker et al.	07-07-98	
	11.	5,811,863	Rostoker et al.	09-22-98	
	12.	5,822,214	Rostoker et al.	10-13-98	
	13.	5,859,449	Kobayashi et al.	01-12-99	
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	15.	5,973,376	Rostoker et al.	10-26-99	
	16.	6,035,108	Kikuchi	03-07-00	
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	18.	6,068,662	Scepanovic et al.	05-30-00	
	19.	6,123,736	Pavisic et al.	09-26-00	
	20.	6,134,702	Scepanovic et al.	10-17-00	
	21.	6,155,725	Scepanovic et al.	12-05-00	
	22.	6,216,252 B1	Dangelo et al.	04-10-01	

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24-AUGUST-2003

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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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## **U.S. PATENTS RELATED TO THE PRESENT APPLICATIONS**

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28.	6,412,097	B1	Kikuchi et al.	06-25-02	

## **FOREIGN PATENT DOCUMENTS**

Examiner* Initials	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code (if known) <sup>5</sup>				
	29.	JP	64-15947		Ouchi	01-19-89		

## **NON PATENT LITERATURE DOCUMENTS**

Examiner* Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	30.	A.H. Farrahi et al., Quality of EDA CAD Tools: Definitions, Metrics and Directions, Quality Electronic Design, 2000, Proceedings of the first International Symposium on March 2000, pp 395-405.	
	31.	C. Chiang, et al., Wirability of Knock-Knee Layouts with 45° Wires, IEEE Transactions on Circuits and Systems, Vol. 38, Issue 6, pp 613-624, June 1991.	
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	33.	J. Vicente, RSR: A New Rectilinear Steiner Minimum Tree Approximation for FPGA Placement and Global Routing, Proceedings of the 24 <sup>th</sup> Euro Micro Conference, pp 192-195, August 1998.	
	34.	K. Powers et al., The 60° Grid: Routing Channels in Width d/square root 3, VLSI, 1991, Proceedings., First Great Lakes Symposium on Kalamazoo, MI, USA, pp 214-291, March 1991.	
	35.	M. Alexander et al., Performance-Oriented Placement and Routing for field-programmable gate arrays, Proceedings of the European Design Automation Conference, pages 80-85, 1995.	
	36.	M. Alexander et al., Placement and Routing for Performance-Oriented FPGA Layout, VLSI Design, Vol. 7, No. 1, 1998.	
	37.	P. Dood, et al. A Two-Dimensional Topological Compactor with Octagonal Geometry, 28 <sup>th</sup> ACM/IEEE Design Automation Conference, pp 727-731, July 1991.	

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				<b>Examiner Name</b>	A.M. Thompson
<b>Sheet</b>	3	of	3	<b>Attorney Docket Number</b>	SPLX.P0017

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38.	P. Parikh, et al., Congestion Driven Quadratic Placement, Proceedings of Design Automation Conference, 1998, pp 275-278.	
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<b>Examiner Signature</b>		<b>Date Considered</b>	24-AUGUST-2003
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